

Name	Multilayer Chip Inductors	COMPOSITE SPECIFICATION		1/8
	PCLS-201212	SPEC#	PCLS2012-100M	

### 1. SCOPE

This specification applies to the EBL-2012 series Multilayer Chip Inductors

### 2. STANDARD ATMOSPHERIC CONDITIONS

Unless otherwise specified the standard range of atmospheric conditions for making measurements and tests is as follows:

Ambient temperature :  $20 \pm 15^\circ\text{C}$

Relative humidity : 30~70%

If there may be any doubt on the results, measurements shall be made within the following limits :

Ambient temperature :  $25 \pm 5^\circ\text{C}$

Relative humidity : 30~70%

### 3. RATINGS

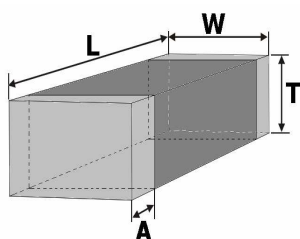
PART NO	INDUCTANCE	Q	SELF-RESONANT	DC RESISTANCE	※
			FREQUENCY(MHz)	( $\Omega$ ) Max	RATED CURRENT
	AT1 MHz 100mV	Min	Min		(mA)Max
PCLS2012-100M	$10\mu\text{H} \pm 20\%$	50	24	$0.56 \pm 25\%$	200

※The maximum rated current : the DC current value having temperature increased  $40^\circ\text{C}$  after thru DC current 2 hours at ambient temperature.

Regarding to the inductance variability of rated current, please refer to page 2:

Inductance Vs. DC superposition characteristics.

### 4. DIMENSION



OPERATING TEMP. RANGE :  $-40^\circ\text{C} \sim +85^\circ\text{C}$

STORAGE TEMP. RANGE :  $-10^\circ\text{C} \sim +40^\circ\text{C}$

TYPE	L	W	T	A(m/m)
PCLS-2012	$2 \pm 0.2$ (0.079 $\pm$ 0.008)	$1.25 \pm 0.2$ (0.049 $\pm$ 0.008)	$1.25 \pm 0.2$ (0.049 $\pm$ 0.008)	$0.2 \sim 0.8$ (0.008~0.031)

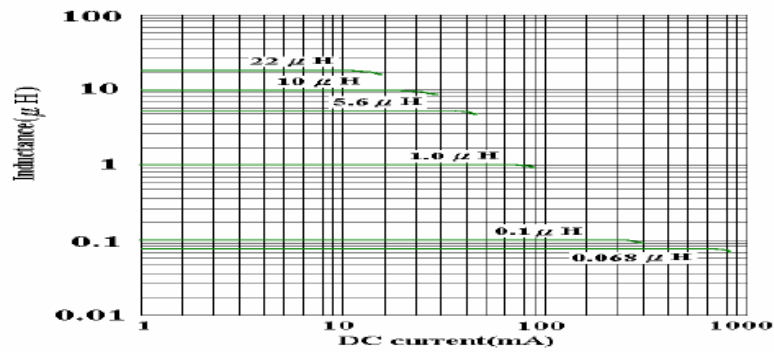
### 5. The place of origin :

Taichung, Taiwan

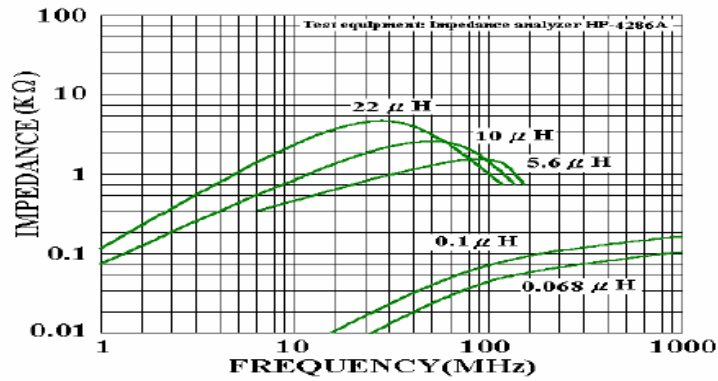
HISTORY	DATE	REVISION		SIGN.	SIGN.
PLANNED BY	CHECKED BY	APPROVED BY			
JSHING	HSU	<b>Chi Chi Huang</b>			

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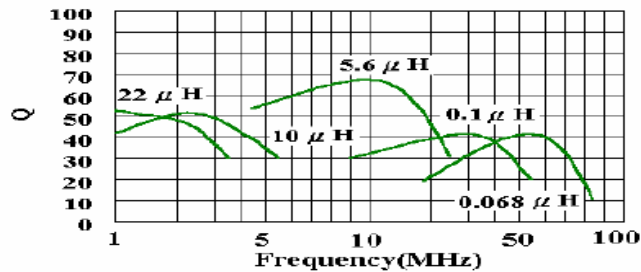
**INDUCTANCE VS DC SUPERPOSITION CHARACTERISTICS**



**IMPEDANCE VS FREQUENCY CHARACTERISTICS**



**Q VS FREQUENCY CHARACTERISTICS**



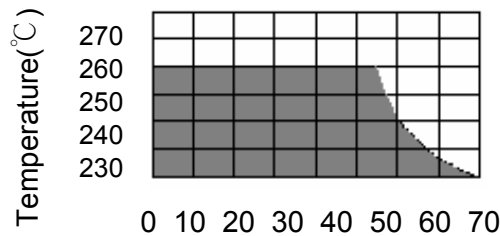
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## 6) Reflow soldering conditions

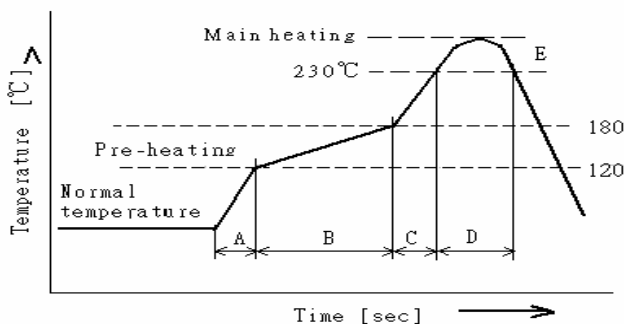
- Pre-heating should be in such a way that the temperature difference between solder and ferrite surface is limited to 150°C max. Also cooling into solvent after soldering should be in such a way that the temperature difference is limited to 100°C max.

Unenough pre-heating may cause cracks on the ferrite, resulting in the deterioration of product quality.

- Products should be soldered within the following allowable range indicated by the slanted line. The excessive soldering conditions may cause the corrosion of the electrode, When soldering is repeated, allowable time is the accumulated time.



## Temperature Profile



A	Slope of temp. rise	1 to 5	°C/sec
B	Heat time	50 to 150	sec
	Heat temperature	120 to 180	°C
C	Slope of temp. rise	1 to 5	°C/sec
D	Time over 230°C	90~120	sec
E	Peak temperature	255~260	°C
	Peak hold time	10 max.	sec
No. of mounting		3	times

(Melting area of solder)

## 6-1 Reworking with soldering iron

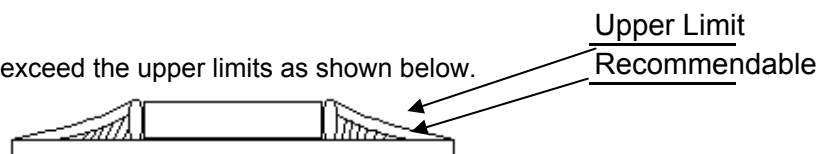
Preheating	150°C, 1minute
Tip temperature	280°C max
Soldering time	3seconds max.
Soldering iron output	30w max.
End of soldering iron	∅ 3mm max.

- Reworking should be limited to only one time.

Note : Do not directly touch the products with the tip of the soldering iron in order to prevent the crack on the ferrite material due to the thermal shock.

## 6-2 Solder Volume

Solder shall be used not to be exceed the upper limits as shown below.



Accordingly increasing the solder volume, the mechanical stress to product is also increased. Exceeding solder volume may cause the failure of mechanical or electrical performance.

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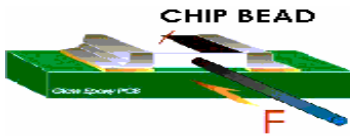
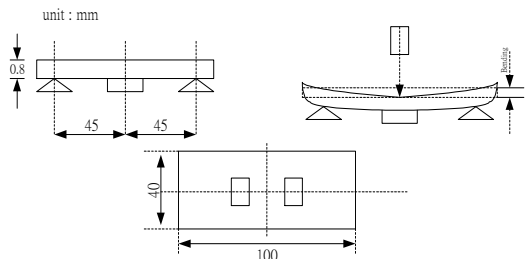
**7 EQUIPMENT**
**7-1 IMPEDANCE**

Impedance shall be measured with HP – 4286A impedance analyzer or equivalent system

**7-2 DC RESISTANCE**

DC resistance shall be measured using HP 4338 digital milli – ohm meter with 4 terminal method.

**8. MECHANICAL CHARACTERISTICS**

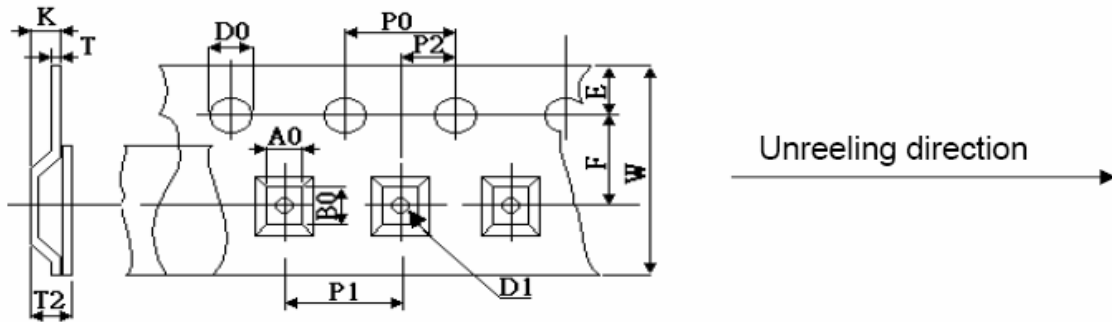
ITEM	Specification	TEST CONDITIONS
TERMINAL STRENGTH	Without deformation cases inductance shall be satisfied $\pm 20\%$ DC resistance shall be satisfied.	Solder chip on PCB and applied 10N (1.02Kgf) for 10 sec 
Substrate bending test	Without deformation cases, inductance shall be satisfied $\pm 20\%$ DC resistance shall be satisfied.	After soldering a chip to a test substrate, bend the substrate by 3mm hold for 10s and then return. Soldering shall be done in accordance with the recommended PC board pattern and reflow soldering. 
RESISTANCE TO SOLDER HEAT	No visible damage Electrical characteristics and mechanical characteristics shall be satisfied.  <b>Consult standard MIL-STD-202 METHOD 210</b>	Solder Temp. : $265 \pm 3^\circ\text{C}$ Immersion time : $6 \pm 1$ sec Preheating : $100^\circ\text{C}$ to $150^\circ\text{C}$ , 1 minute. Measurement to be made after keeping at room temp for $24 \pm 2$ hrs. Solder : Sn-3Ag-0.5Cu
SOLDER – ABILITY	95% min. coverage of all metallized area  <b>Consult standard J-STD-002</b>	Solder temp. : $240 \pm 5^\circ\text{C}$ Immersion time : $3 \pm 1$ sec Solder : Sn-3Ag-0.5Cu

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<p>9. RELIABILITY AND TEST CONDITIONS</p> <p>9-1 HIGH TEMPERATURE RESISTANCE</p> <p>a. Performance specification</p> <p>1. Appearance : no mechanical damage</p> <p>2. Inductance shall be with <math>\pm 20\%</math> of the initial value</p> <p>b. Test condition</p> <p>1. Temperature <math>+85^{\circ}\text{C} \pm 2^{\circ}\text{C}</math></p> <p>2. Applied current : Rated current</p> <p>3. Testing time : <math>1008 \pm 12</math> hrs (maximum value)</p> <p>4. Measurement : After placing at room ambient temperature for 24 hours minimum</p> <p>9-2 HUMIDITY RESISTANCE</p> <p>a. Performance specification</p> <p>1. Appearance : no mechanical damage</p> <p>2. Inductance shall be with <math>\pm 20\%</math> of the initial value</p> <p>b. Test condition</p> <p>1. Humidity : 90 to 95% RH</p> <p>2. Temperature : <math>60 \pm 2^{\circ}\text{C}</math></p> <p>3. Applied current : Rated current (maximum value)</p> <p>4. Testing time : <math>1008 \pm 12</math> hours</p> <p>5. Measurement : After placing at room ambient temperature for 24 hours minimum</p> <p>9-3 TEMPERATURE CYCLE</p> <p>a. Performance specification</p> <p>1. Appearance : no mechanical damage</p> <p>2. Inductance shall be with <math>\pm 20\%</math> of the initial value</p> <p>b. Test condition</p> <p>1. Temperature <math>-40^{\circ}\text{C}, +85^{\circ}\text{C}</math> kept stabilized for 30 minutes each</p> <p>2. Cycle : 100 cycles</p> <p>3. Measurement : After placing for 24 hours minimum at room ambient temperature</p> <p>4. step1. <math>-40^{\circ}\text{C}</math> temp <math>\pm 3^{\circ}\text{C}</math> <math>30 \pm 3</math> minutes</p> <p>step2. Room temperature 2 to 5 minutes</p> <p>step3. <math>+85^{\circ}\text{C}</math> temp <math>\pm 2^{\circ}\text{C}</math> <math>30 \pm 3</math> minutes</p> <p>step4. room temperature 2 to 5 minutes</p> <p>9-4 LOW TEMPERATURE STORAGE LIFE TEST</p> <p>a. Performance specification</p> <p>1. Appearance : no mechanical damage</p> <p>2. Inductance shall be with <math>\pm 20\%</math> of the initial value</p> <p>b. Test condition</p> <p>1. Temperature <math>-40^{\circ}\text{C} \pm 2^{\circ}\text{C}</math></p> <p>2. Testing time : <math>1008 \pm 12</math> hours</p> <p>3. Measurement : After placing for 24 hours minimum at room ambient temperature</p> <p>9-5 THERMAL SHOCK</p> <p>a. Performance specification</p> <p>1. Appearance : no mechanical damage</p> <p>2. Inductance shall be with <math>\pm 20\%</math> of the initial value</p> <p>b. Test condition</p> <p>1. Temperature <math>-40^{\circ}\text{C}, +85^{\circ}\text{C}</math> kept stabilized for 30 minutes each</p> <p>2. Cycle : 100 cycles</p> <p>3. Measurement : After placing for 24 hours minimum at room ambient temperature</p> <p>9-6 VIBRATION TEST</p> <p>a. Performance specification</p> <p>1. Appearance : no mechanical damage</p> <p>2. Inductance shall be with <math>\pm 20\%</math> of the initial value</p> <p>b. Test condition</p> <p>1. Waveform: Sine wave</p> <p>2. Frequency: 10~55~10 Hz</p> <p>3. Sweep time: 1 min</p> <p>4. Amplitude: 1.5 mm (peak-peak)</p> <p>5. Direction: X, Y, Z (3 axes)</p> <p>6. Duration: 2 hrs./axis, total 6 hrs.</p>				

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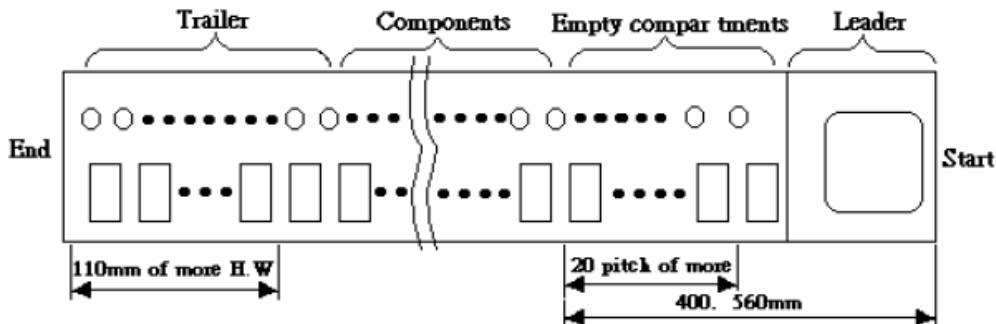
**EMBOSSED CARRIER TAPE PACKING**

**10-1 DIMENSIONS**

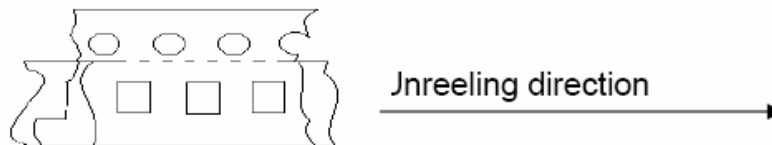


A0	B0	W	F	E	P1	P2	P0	D0	D1	K	T	T2
1.5	2.35	8.0	3.5	1.75	4.0	2.0	4.0	1.55	1.0	1.67	0.22	1.67
±0.1	±0.1	±0.1	±0.05	±0.1	±0.1	±0.05	±0.1	±0.05	±0.1	±0.15	±0.05	±0.25

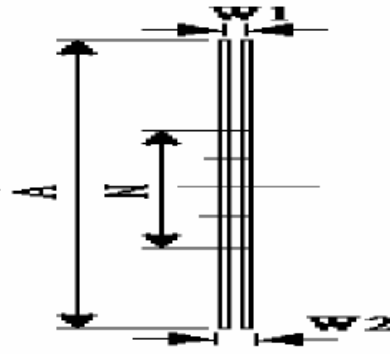
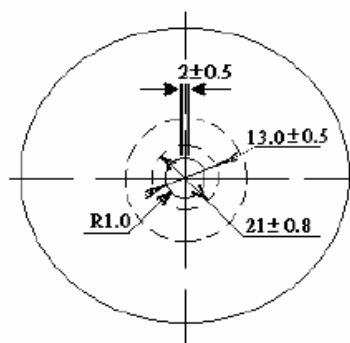
**10-2 LEADER AND TRAILER TAPE**



**10-3 DIRECTION THE DIRECTION SHALL BE SEEN FROM THE TOP OF COVER TAPE**



**10-4 REELS**



UNIT:mm

A	178 ±2.0
N	50 MIN
W1	10 ±1.5
W2	20 MAX

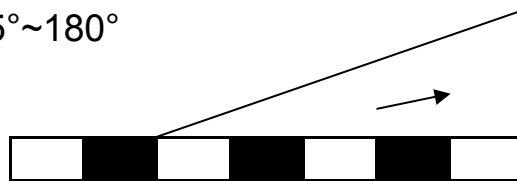
PACKING QTY.  
3,000 PCS REEL

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### 10-5 PEELING STRENGTH OF COVER TAPE

Cover tape	(10g~100g)
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165°~180°



#### Test condition

- 1) peel angle : 165°~180° vs carrier tape
- 2) peel speed : 300mm/min

#### 11.PACKAGING

- 1) Tape & Reel packaging in composite specification 6/8
- 2) Reel and a bag of desiccant shall be packed in Nylon or plastic bag
- 3) Maximum of 5 reels shall be packaged in a inner box
- 4) Maximum of 6 inner box shall be packaged in a outer box

#### 12.Reel Label

Producing the goods label needs to indicate (1 ) Pb Free (2) RoHS Compliant

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<p>13. STORAGE</p> <p>13-1 The solderability of the external electrode may be deteriorated if packages are stored where they are exposed to high humidity. Packages must be stored at 40°C or less and 70% RH or less.</p> <p>13-2 The solderability of the external electrode may be deteriorated if packages are stored where they are exposed to dust or harmful gas (hydrogen chloride, sulfurous acid gas or hydrogen sulfide).</p> <p>13-3 Packaging material may be deformed if packages are stored where they are exposed to heat or direct sun – light.</p> <p>13-4 Minimum packages, such as polyvinyl heat – seal packages shall not be opened until just before they are used. If opened, use the reels as soon as possible.</p> <p>13-5 Solderability specified in composite specification 4/8 shall be for 6 months from the date of delivery on condition that they are stored at the environment specified clause 13-1 &amp; 13-2. For those parts which passed more than 6 months shall be checked solderability before it is used.</p> <p>14. Quality System</p> <ul style="list-style-type: none"> <li>■ ISO/TS16949</li> <li>■ IECQ QC 080000</li> </ul>				